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To:	U.S. Patent Office	From: B. Noël Kivlin
Attn:	Examiner Cynthia H. Britt	Pages: 6
Fax:	571-273-8300	Date: June 22, 2006
Re:	5957-80904	Phone: (512) 853-8840

Comments:

Attached is a Response to Office Action of June 14, 2006 with regard to U.S. patent application 10/643,187.

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Application Serial No. 10/643,187 - Filed August 19, 2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JUN 2 2 2006

Application No.: 10/643,187 Examiner: Cynthia H. Britt Filed: August 19, 2003 Group/Art Unit: 2138 Inventor(s): *\$\text{co}* Atty. Dkt. No: 5957-80904 Katsuki Hazama I hereby certify that this correspondence is being faxed to Commissioner for Patents at 571-273-8300, on the date indicated Title: Multilevel semiconductor B. Noël Kivlin memory, write/read method Printed Name thereto/therefrom and storage medium storing June 22, 2006 Signaturo Date write/read program §

RESPONSE TO OFFICE ACTION OF JUNE 14, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of June 14, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.